

Declaration and Power of Attorney for Patent Application

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name; I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

METHOD AND SYSTEM FOR LOGIC VERIFICATION USING MIRROR INTERFACE

the specification of which (check one)

☒

is attached hereto.

☐

was filed on _____ as Application Serial No. _____ and was amended on _____

I hereby state that I have reviewed and understand the contents of the above- identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s):

Number	Country	Day/Month/Year	Priority Claimed
--------	---------	----------------	------------------

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information material to the patentability of this application as defined in Title 37, Code of Federal Regulations, §1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Prior U.S. Applications:

Serial No.	Filing Date	Status
------------	-------------	--------

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

As a named inventor, I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: **Mark F. Chadurjian, Reg. No. 30,739; Richard A. Henkler, Reg. No. 39,220; Richard M. Kotulak, Reg. No. 27,712; James M. Leas, Reg. No. 34,372; William D. Sabo, Reg. No. 27,465; Eugene I. Shkurko, Reg. No. 36,678; Robert A. Walsh, Reg. No. 26,516; Howard J. Walter, Jr., Reg. No. 24,832; Christopher A. Hughes, Reg. No. 26,914; Edward A. Pennington, Reg. No. 32,588; John E. Hoel, Reg. No. 26,279 and Joseph C. Redmond, Jr., Reg. No. 18,753. George Vande Sande, Reg. No. 17,276, Burton A. Amernick, Reg. No. 24,852, Richard Wiener, Reg. No. 18,741, Townsend M. Belser, Jr., Reg. No. 22,956; Morris Liss, Reg. No. 24,510, Martin Abramson, Reg. No. 25,787, George R. Pettit, Reg. No. 27,369, Elzbieta Chlopecka, Reg. No. 32,767, Eric J. Franklin, Reg. No. 37,134, and William E. Curry, Reg. No. 43,572, David W. Ward, Reg. No. 45,198, and John A. Evans, Ph.D., Reg. No. 44,100; all of POLLOCK, VANDE SANDE & AMERNICK**

Send all correspondence to: William E. Curry
Pollock, Vande Sande & Amernick, R.L.L.P.
PO Box 19088
Washington, DC 20036-3425 U.S.A.

Direct Telephone Calls to: William E. Curry, Reg. No. 43,572 (202) 331-7111

(1) Inventor: Robert J. Devins

Signature: 

1/10/01
Date

Residence: 10 Creek Road, Essex Junction, VT 05452

Citizenship: USA

Post Office

Address: Same as residence.

(2) Inventor: Paul J. Ferro

Signature: 

1/10/01
Date

Residence: 41 Butler Drive, South Burlington, VT 05403

Citizenship: USA

Post Office

Address: Same as residence.

(3) Inventor: Peter D. LaFauci

Signature: _____

Date

Residence: 5001 Sunset Forest Circle, Holly Springs, NC 27540

Citizenship: USA

Post Office

Address: Same as residence.

(4) Inventor: Kenneth a. Mahler

Signature: Kenneth A Mahler

1/10/01
Date

Residence: 16 Heatherbush Road, Essex Junction, VT 05452

Citizenship: USA

Post Office

Address: Same as residence.

(5) Inventor: David W. Milton

Signature: David W Milton

1/10/01
Date

Residence: 12 Park Street, Underhill, VT 05489

Citizenship: USA

Post Office

Address: Same as residence.

In Re: Devins, et al.
Serial No.:
Filed: Herewith
Title: Method and System for Logic Verification
Using Mirror Interface

Date: March 26, 2001
Group Art Unit:
Examiner:

**DECLARATION OF PERSON MAKING THIS STATEMENT OF FACTS
IN SUPPORT OF FILING ON BEHALF OF NON-SIGNING INVENTOR
(37 C.F.R. § 1.47)**

Maryann Luisi
International Business Machines Corporation
Intellectual Property Law - Mail 972E
1000 River Street
Essex Junction, Vermont 05452

My name is Maryann Luisi. I have been the Operation Specialist working in the Intellectual Property Law Group of IBM located in Burlington, Vermont since 1996 with previous work experience in this department dating back to 1986. Among other administrative duties, one of my responsibilities is to assist attorneys in locating and setting up communications between IBM and inventors no longer working for IBM. In the overwhelming majority of situations with ex-employees, we are able to obtain their cooperation. Our office has on an occasion paid a small honorarium when absolutely necessary to enlist an ex-employee to sign. Attached is Mr. Lafauci's employment agreement which provides for such cooperation.

On January 17, 2001, I had a lengthy discussion with Mr. Peter D. Lafauci, who is one of the inventors on our docket, BUR9-2000-0154-US1, "Method and System for Logic Verification Using Mirror Interface". Peter seemed very resistant to cooperating in the review of BUR9-2000-0154-US1. He gave me a number of reasons why, from the requirement that he would need to hire an attorney, to time away from his family and his job. He also felt that he would have to thoroughly discuss the verbiage in the patent application with all of his co-inventors. All of these items combined would have amounted to what Mr. Lafauci believes would have been a considerable amount of time on his part. I suggested that we had on occasion offered money up front. He rejected that idea. It became apparent to me that it would be very costly for IBM to have him review and sign. He left me with the definite impression that he was not interested in solving the problem at all.

So I discussed the case with our department manager/counsel, Mark F. Chadurjian. It was suggested that Richard M. Kotulak, who is the assigned attorney on this patent application, attempt to get in touch with Mr. Lafauci and discuss the matter with him. Upon information and belief, both he and Robert Devins, one of the co-inventors did place calls into Mr. Lafauci, leaving voice mail messages. Neither received a response from Mr. Lafauci.

In accordance with patent office rules, Richard M. Kotulak and his administrative analyst sent the attached letter to Mr. Lafauci along with a final draft of BUR9-2000-0154-US1. The letter and draft were

received by Mr. Lafauci's wife on February 20, 2001 (Receipt attached.) In that letter, we again offered an honorarium to enlist his cooperation. Again we received no response.

On March 9, 2001, I was able to further discuss the matter with Mr. Lafauci over the phone. He refused to cooperate indicating that he was not prepared to sign at this time and he would be returning the forms unsigned to our office at some point in the future.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Sincerely yours,


Maryann Luisi
Operations Specialist

20000304 040404